

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	messag\$4 with (((thread near circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2009/06/18 10:51
L2	1186	messag\$4 with (((thread circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2009/06/18 10:51
L3	0	messag\$4 with (((thread near circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2009/06/18 10:52
L4	0	messag\$4 with (((thread near circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 10:52
L5	0	messag\$4 with (((thread near circuit) ) and(integrated near circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 10:53
L6	10	(((thread near circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 10:53
L7	0	(((thread near circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and messag\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 10:53
L8	8	(((thread near circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and (signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 10:53

L9	3	((thread near circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and (signal near control)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 10:54
L10	0	((thread near circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and message	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:31
L11	208	((thread same circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and message	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:32
L12	59	((thread same circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and (message with thread)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:32
L13	57	((thread same circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and (message with thread) and (control)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:32
L14	10	((thread same circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and ((message with thread) with (control))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:33
L15	0	design\$5 with ((thread near circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and ((message with thread) with (control))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:34

L16	0	design\$5 with ((thread same circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and ((message with thread) with (control))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:35
L17	1	design\$5 with ((thread same circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and ((message with thread) )	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:35
L18	2	design\$5 with ((thread same circuit) same (connect\$5 or interconnect\$5)) and (integrated near circuit) and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:36
L19	2	design\$5 with ((thread same circuit)) and (connect\$5 or interconnect\$5) and (messag\$5 with (integrated near circuit)) and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:47
L20	44519	design\$5 with ((thread same circuit)) and (connect\$5 or interconnect\$5) and (messag\$5 ) (integrated near circuit) and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:47
L21	6	design\$5 with ((thread same circuit)) and (connect\$5 or interconnect\$5) and (messag\$5 ) and (integrated near circuit) and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:48
L22	0	design\$5 with ((thread same circuit)) and ( inter near connect \$5) and (messag\$5 ) and (integrated near circuit) and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:50

L23	0	design\$5 with ((thread same circuit)) and ( inter near connect \$5) and (signal ) and (integrated near circuit) and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:50
L24	12	design\$5 with ((thread same circuit)) and ( interconnect\$5) and (integrated near circuit) and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:51
L25	19	design\$5 with ((thread same circuit) same ( interconnect\$5)) and (integrated near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:53
L26	17	design\$5 with ((thread same circuit) same ( interconnect\$5)) and (integrated near circuit) and database	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 11:53
L27	2	"20060190897"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 12:20
L28	29	((thread same circuit) same ( interconnect \$5)) and (integrated near circuit) and database	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/18 12:24

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